



Substitute form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete If Known <table border="1"> <tr> <td>Application Number</td> <td>10/813,226</td> </tr> <tr> <td>Filing Date</td> <td>March 30, 2004</td> </tr> <tr> <td>First Named Inventor</td> <td>Honary, Hooman</td> </tr> <tr> <td>Art Unit</td> <td>2183</td> </tr> <tr> <td>Examiner Name</td> <td>Unknown</td> </tr> </table>		Application Number	10/813,226	Filing Date	March 30, 2004	First Named Inventor	Honary, Hooman	Art Unit	2183	Examiner Name	Unknown
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Sheet	1	of	2	Attorney Docket No: 80107.115US1											

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	†
/S.C./	WO-05094016A2	10/06/2005	Honary, H., et al.	
/S.C./	WO-2005094016A3	10/06/2005	Honary, H.	

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/Sai Ming Chan/

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06/18/2008

Substitute Disclosure Statement Form (PTO-1449)

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <th colspan="2" style="text-align: center; font-size: small;">Complete if Known</th> </tr> <tr> <td style="width: 50%;">Application Number</td> <td>10/813,226</td> </tr> <tr> <td>Filing Date</td> <td>March 30, 2004</td> </tr> <tr> <td>First Named Inventor</td> <td>Honary, Hooman</td> </tr> <tr> <td>Art Unit</td> <td>2183</td> </tr> <tr> <td>Examiner Name</td> <td>Unknown</td> </tr> </table>		Complete if Known		Application Number	10/813,226	Filing Date	March 30, 2004	First Named Inventor	Honary, Hooman	Art Unit	2183	Examiner Name	Unknown
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	†
/S.C./		<u>International Search Report and Written Opinion of the International Searching Authority</u> ; Dated 12/06/2005; PCT/US2005/010387, 16 pgs.	
/S.C./		ALBANESI, M. G., et al., "A VLSI 128-Processor Chip for Multiresolution Image Processing", <u>Proceedings of the First International Conference on Ischia</u> , (May 02, 1994), 296-307 pgs.	
/S.C./		MARESCAUX, T., et al., "Networks on Chip as Hardware Components of an OS for Reconfigurable Systems", <u>FIELD-PROGRAMMABLE LOGIC AND APPLICATIONS, 13TH INTERNATIONAL CONFERENCE</u> , Vol. 2778, (2003), 595-605 pgs.	
/S.C./		ZHANG, et al., "Interconnect Architecture Exploration for Low-Energy Reconfigurable Single-Chip DSPs", <u>VLSI '99, PROCEEDINGS, IEEE COMPUTER SOCIETY WORKSHOP ON ORLANDO, FL</u> , (April, 08, 1999), 2-8 pgs.	

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